

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S3	2838	(non\$1overlap\$4 non adj overlap \$4 nonoverlap \$4) near2 clock \$4	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/07/25 17:19
S4	578397	(latch flip adj flop flip\$1flop)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/07/25 17:19
S5	688	S3 same S4	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/07/25 17:19
S6	92	S3 same S4 with delay	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/07/25 17:19
S15	545	S5 not S6	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/25 18:21
S16	157	S15 and (bounc \$4 race adj through\$4 rac\$4 race\$1through)	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/25 18:21
S22	388	S15 not S16	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/25 19:06
S23	2838	(non\$1overlap\$4 non adj overlap \$4 nonoverlap \$4) near2 clock \$4	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/07/26 19:05
S24	90	master with slave with S23	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/26 19:05
S25	63	S24 and delay	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/26 19:06
S26	27	S24 not S25	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/26 19:11

S29	3	latch with NAND with por with inverter	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/07/26 20:05
S30	3713	latch with "NAND" with inverter	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/07/26 20:08
S31	122	latch with "NAND" with inverter with cross adj coupl \$4	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/07/26 20:08

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